

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10699764	
	Filing Date		2003-11-03	
	First Named Inventor	Albert Sun et al.		
	Art Unit	2186		
	Examiner Name	Chase W. Peers		
Attorney Docket Number		MXIC 1520-1		

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	1	WO 00/52825		A1	2000-09-08	Altera Corporation		<input type="checkbox"/>
	2	06-068320	JP		1994-11-03	Ricoh Co., Ltd.		<input checked="" type="checkbox"/>

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	1	"Emergence of microcomputer with peripheral circuitry composed of FPGA," Nikkei Electronics 2000.3.27 (no. 766) p218.	<input checked="" type="checkbox"/>
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- ☐ See attached certification statement.
- ☐ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
- ☐ None

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/markhaynes/	Date (YYYY-MM-DD)	2006-06-29
Name/Print	Mark Haynes	Registration Number	30,846

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